

AccessionIndex: TCD-SCSS-T.20121208.011
Accession Date: 8-Dec-2012
Accession By: Dr.Jeremy Jones
Object name: (Unknown?) Diode ROM Array
Vintage: c.195x
Synopsis: Low-density vintage diode ROM memory array.

Description:

This is a low-density vintage diode 20 x 8 ROM memory array, made by ????, most likely in the U.K. (as it was given to Dr.Jeremy Jones by his father, who had worked in the UK computing industry).

On each side there is an independent 10 x 8 array of field-programmable locations, where metal-encased diodes are inserted to program a crosspoint connection between rows and columns. The rows are labelled 0-9. The columns are labelled M1-8 and subtitled 'Tape Channels', but each front column is wired to the rear column behind it, so the rear columns are labelled in reverse order.

At the top front, between the columns are extra contacts labelled A-H and subtitled 'Digits', with an extra label that F = DB1, and only E-H wired. Similarly, at the top rear, between the columns are further extra contacts labelled A-H (in the reverse order) and subtitled 'Symbols', with an extra label that B = DB2, but in contrast with the front all are wired.

It may be pertinent that in the early UK computer industry (before the introduction of the term 'byte', most notably from the USA with the IBM 360 series), an 8-bit field was known as a 'syllable', and a bit was known as a 'binary digit', very often abbreviated to 'digit'.

The array is mounted on a wooden base with an AMD Am2716 16k x 8 EPROM c.1979 attached for comparison. Overall it is ?? cm high, ?? cm wide, and ?? cm deep.

The homepage for this catalog is at: <https://www.scss.tcd.ie/SCSSTreasuresCatalog/>
Click '*Accession Index*' (1st column listed) for related folder, or '*About*' for further guidance. Some of the items below may be more properly part of other categories of this catalog, but are listed here for convenience.

Accession Index	Object and Identification
TCD-SCSS-T.20121208.011	(Unknown?) Diode ROM Array, Low-density vintage diode ROM memory array, c.195x. S/N: none



Figure 1: (Unknown?) Diode ROM Array three-quarter view

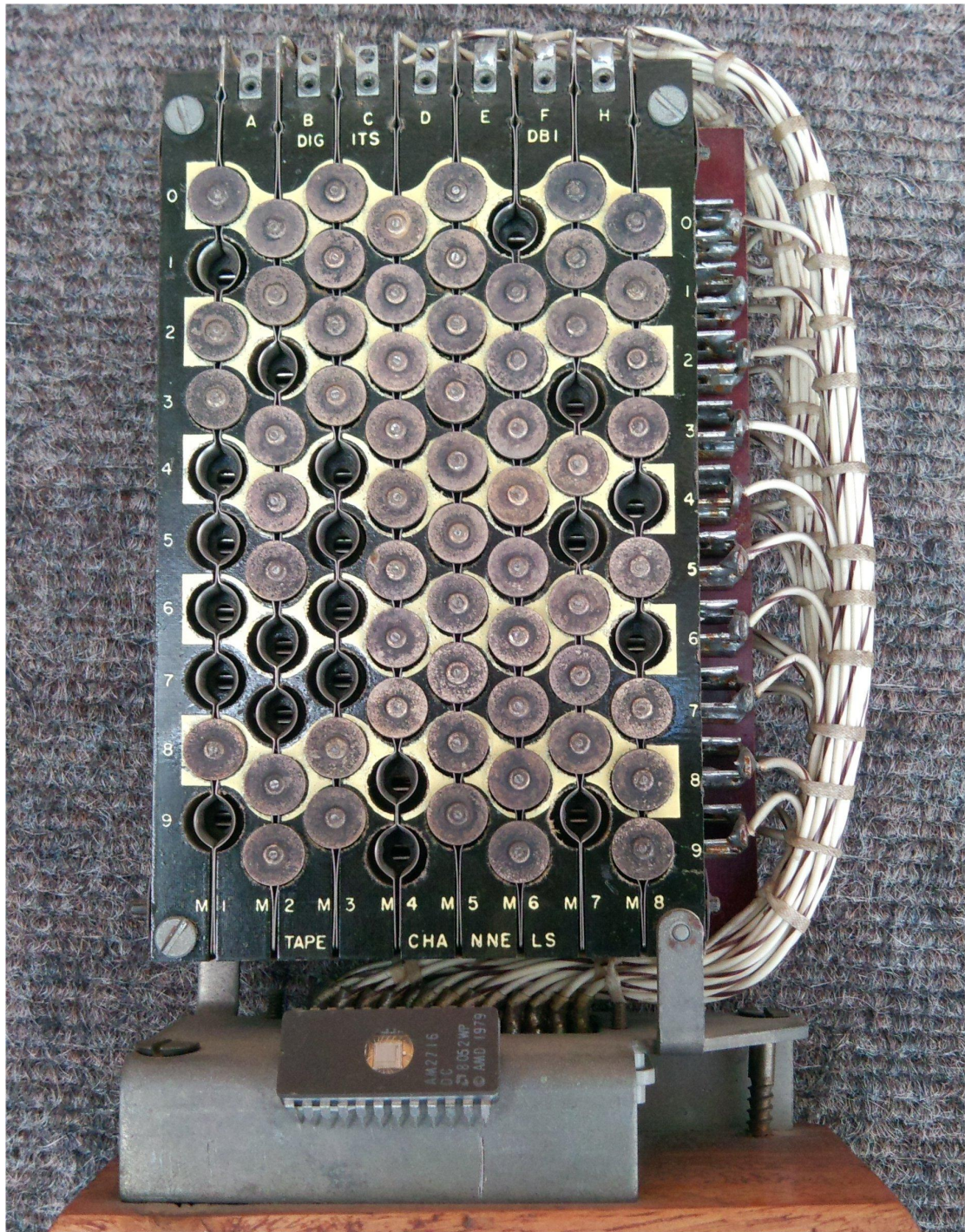


Figure 2: (Unknown?) Diode ROM Array front view



Figure 3: (Unknown?) Diode ROM Array left front view

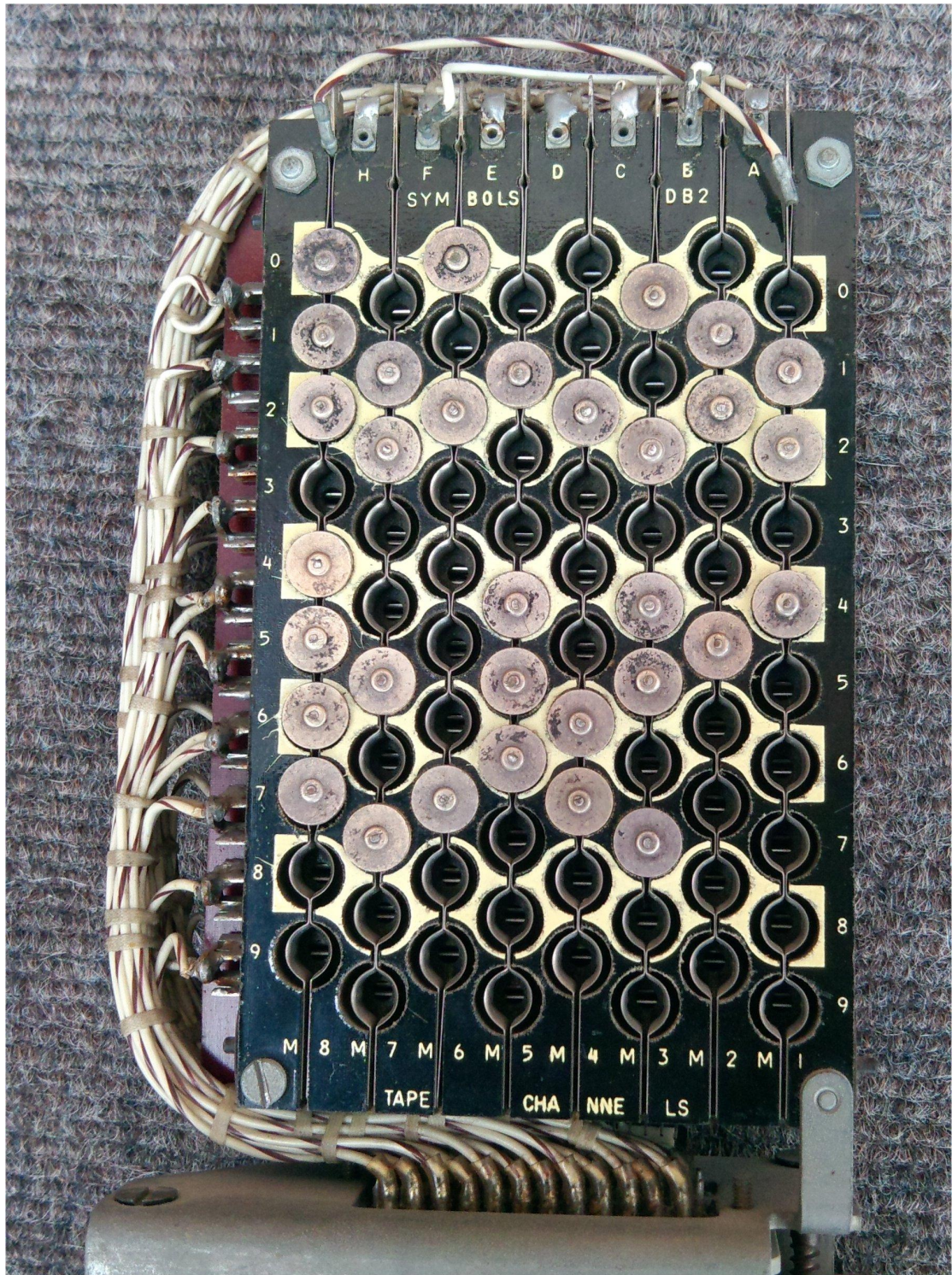


Figure 4: (Unknown?) Diode ROM Array rear view



Figure 5: (Unknown?) Diode ROM Array right rear view